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Re: U.S. Patent Application Serial No. 10/802,549
For: ADJUSTING DIE PLACEMENT ON A
SEMICONDUCTOR WAFER TO INCREASE YIELD
By: Eitan CADOURI
Our Reference: 52432-20006.00

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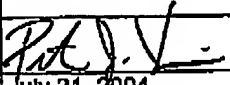
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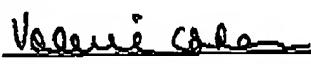
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TRANSMITTAL FORM <small>(to be used for all correspondence after initial filing)</small>	Application Number	10/802,549	
	Filing Date	March 16, 2004	
	First Named Inventor	Eitan CADOURI	
	Group Art Unit	2812	
	Examiner Name	Not Yet Assigned	
Total Number of Pages In This Submission	2	Attorney Docket Number	524322000800

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Docket No.: 524322000600
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Eitan CADOURI

Application No.: 10/802,549

Group Art Unit: 2812

Filed: March 16, 2004

Examiner: Not Yet Assigned

For: ADJUSTING DIE PLACEMENT ON A
SEMICONDUCTOR WAFER TO INCREASE
YIELD

POWER OF ATTORNEY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The undersigned hereby appoints the following attorneys and/or agents to prosecute this application and transact all business in the U.S. Patent and Trademark Office connected herewith:

All practitioners at Customer Number 20872.

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Dated: 7/20/04

P. Steven Melman, CFO and Vice President
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